

# INFORMATION DISCLOSURE CITATION

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APPLICANT(S)

Scott Thomas Marcotte

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JCS58 U.S. PTO  
06/442906

11/18/99

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SV	AA	5,202,971	04/13/93	Henson et al.	395	425	
SV	AB	5,594,863	01/14/97	Stiles	395	182.13	
SV	AC	5,740,367	04/14/98	Spilo	395	200.31	
SV	AD	5,745,756	04/28/98	Henley	395	620	
SV	AE	5,799,150	08/25/98	Hamilton et al.	395	200.33	
SV	AF	5,838,916	11/17/98	Domenikos et al.	395	200.49	

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

SV	AG	"Multiple Caching Schemes In A Lan-Attached Server," IBM Technical Disclosure Bulletin, Vol. 38, No. 04, April 1995, pp.143-145
SV	AH	"Warp Server (SMB) Authentication From A Windows NT Client," IBM Technical Disclosure Bulletin, Vol. 40, No. 10, October 1997, pp. 99-100.

EXAMINER

Steve Walker

DATE CONSIDERED

5/17/02

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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**EN99912R**

**APPLICANT(S)**

**Scott Thomas Marcotte**

FILING DATE

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SV		AI	"Programmable High Performance Memory Buffer," IBM Technical Disclosure Bulletin, Vol. 36, No. 10, October 1993, pp. 569-570.

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